SPECIFICATIONS

Dot matrix Display点阵产品规格书





MODEL: TOP-CA-56711BS

上海鼎辉科技股份有限公司

SHANGHAI TOPLITE TECHNOLOGY CO.,LTD.

www.ledtoplight.com.cn www.ledtoplite.com



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TECHNICAL DATA SHEET

TOP-CA-56711BS FOR 7×11 / 5×6.5MM MATRIX

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1. FEATURES

- * Low power requirement,
- **%** Solid state reliability.
- **%** Wide viewing angle.
- **X** Easy mounting on P.C. boards.
- **%** RoHS compliant.

2. DESCRIPTION

- **XTOP-CA-56711BS** is a 1.8 inch (45mm) matrix height 7×11 matrix display.
- * This device is made with white dots and black surface.

3. APPLICATION

- Digital readout display.
- Instrument panels.
- **※** Elevator.

PART NO.	SIZE	CHIP EMITTED COLOR	FACE COLOR
TOP-CA-56711BS	1.8 inch (45mm) matrix height	Super Red	Black



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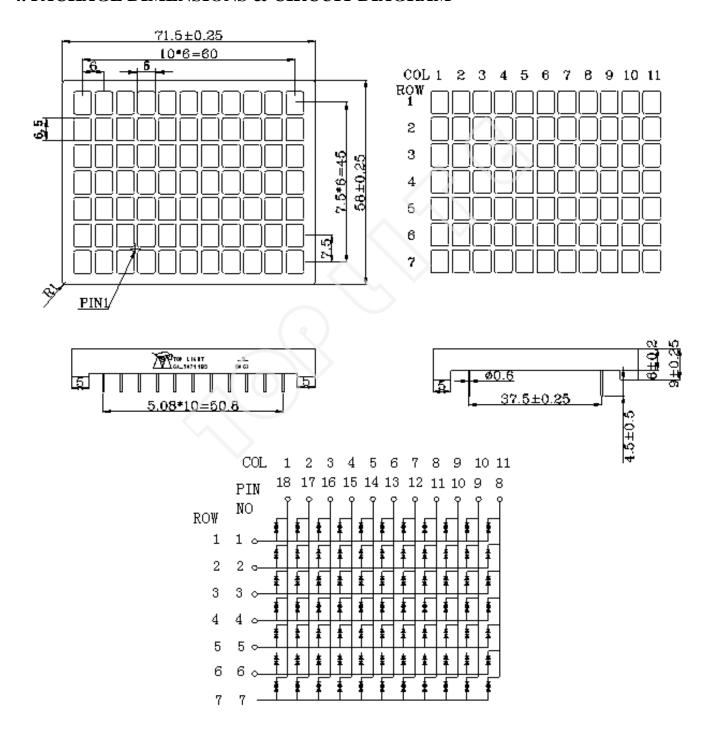
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4. PACKAGE DIMENSIONS & CIRCUIT DIAGRAM





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5. ELECTRICAL/OPTICAL CHARACTERISTIC

5-1. ABSOLUTE MAXIMUM RATINGS (Ta=25°C)

PARAMETER	SYMBOL	VALUE	UNIT
Reverse Voltage	V _R	5	V/dot
Forward Current	I_{F}	30	mA/dot
Peak Forward Current (1/10 Duty Cycle)	I_{PEAK}	120	mA/dot
Power Dissipation	P_{D}	80	mW/dot
Operating Temperature Range	$T_{\rm A}$	- 25 ~ + 85	$^{\circ}$ C
Storage Temperature Range	$T_{ m STG}$	- 30 ~ + 85	$^{\circ}$ C
Solder Temperature	Tsol	260/3	°C/s

5-2. ELECTRICAL/OPTICAL CHARACTERISTICS (Ta=25°C)

PARAMETER	SYMBOL		MIN.	TYP.	MAX.	UNIT	TEST CONDITIONS
		R	13500	15524	17549	ucd	I _F =10mA
Luminous Intensity	I_{V}	S	17550	21937	26325		
		Т	26326	32907	39489		
Forward Voltage	V _F		1.80	2.10	2.40	v/dot	I _F =20mA
Chromatographer			-	635	-	nm	I _F =20mA
Spectral Line Half-Width	Δλ		-	20	-	nm	I _F =20mA
Reverse Current	I_R		-	-	20-	uA	V _R =5v



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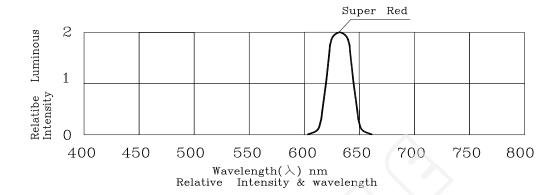
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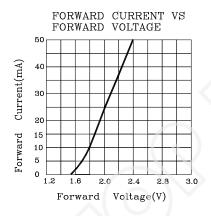
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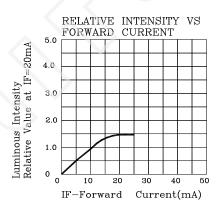
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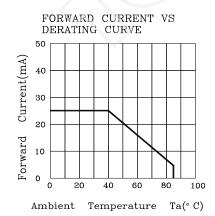
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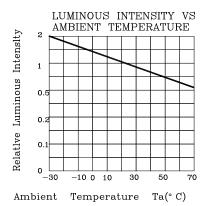
5-3. ELECTRICAL/OPTICAL CHARACTERISTIC CURVES











6. QUALITY CONTROL AND ASSURANCE



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CLASSIFICATION	TEST ITEM	TEST CONDITION	
	OPERATION LIFE	Ta=under room temperature If=12mA-25mA per segment or Ip=80mA/duty=1/8,Pw=1.25mS Ip=160mA/duty=1/16,Pw=1.mS(DOT) Test time=1000HRS(-24HRS+72HRS)	
	MOISTURE	Ta=65°C±5°C RH=90-95% Test time=240HRS±2HRS	
ENDUTRANCE TEST	HIGH TEMPERATURE HIGH HUMIDITY REVERSE BIAS	Ta=65°C±5°C RH=90-95% VR=5V Test time=500hrs(-24HRS+48HRS)	
	HIGH TEMPERATURE STORAGE	To evaluate device's durability for long term storage in high temperature $Ta=85^{\circ}\text{C}\pm5^{\circ}\text{C} \qquad \text{Test time}=1000\text{HRS}(\text{-}24\text{HRS}+72\text{HRS})$	
	LOW TEMPERATURE STORAGE	Ta=-35°C±5°C Test time=1000HRS(-24HRS+72HRS)	
	TEMPERATURE CYCLING	Ta=85°C \sim 25°C \sim -35°C time=30min 5min 30min 5min Cycle test:10cycles	
ENVIRONMENTAL	THERMAL SHOCK	Ta=85°C±5°C ~-35°C±5°C time=10min 10min Cycle test:10cycles	
TEST	SOLOER RESISTANCE	T.sol=260°C±5°C time=10±1sec	
	SOLOER ABILITY	T.sol=230°C±5°C time=5±1sec	

7. SOLDERING CONDITIONS



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The recommended conditions for soldering are as follows.

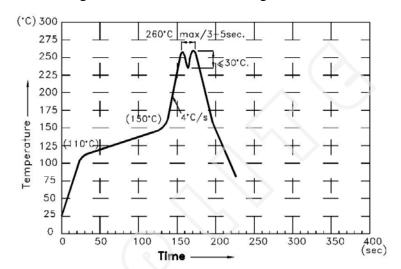
Because the component is made with epoxy resin, the units are susceptible to heat. Therefore, the preheating and soldering temperatures should be kept as low as possible to avoid damage.

7-1. Manual Soldering Conditions(with 1.5mm Iron tip).

Iron Tip Temperature: 350°C Max, Time: 3s Max.

Position: The iron should be situated at least 2mm away from the root of the leads.

7-2. Through the Wave Soldering Conditions Wave Soldering Profile For Lead-free Through-hole LED.



7-3. Soldering General Notes:

- a. Recommend manual soldering to be used only for repair and rework purposes. The soldering iron should not exceed 30W in power. The tip of the soldering iron should not touch the reflector case to avoid heat-damage.
- b. Maintain the pre-heat and peak temperatures with dip units as low as possible and the times as short as is feasible, since the products are susceptible to heat during flow soldering.
- c. After soldering, least three minutes for the component to cool to room temperature before further operations.
- d. If components will undergo multiple soldering processes, or other processes where the components may be subjected to intense heat, please check with toplight for compatibility.